

IN THE SPECIFICATION

1. Please amend the title on page 1 of the application as follows:

**VOLTAGE SUPPLY CIRCUIT FOR REDUCING
POWER LOSS THROUGH A GROUND CONNECTION**

2. Please amend the abstract on page 8 of the application as follows:

A circuit ~~comprises~~ includes a first transistor ~~JFET (21)~~ connected in series to a second transistor ~~JFET (22)~~ ~~wherein a~~ A gate (26) of the first transistor JFET (21) is connected to a source of the second transistor JFET (22) and is The gate of the first transistor may also be connected to a load (30) and is may not be connected to ground. The A gate of the second transistor JFET (22) is may be connected to ground. The transistors ~~JFETs~~ are preferably formed by silicon on insulator integration technology.

(Fig. 2)